Abstracts

Efficiency improvement techniques at low power levels for linear CDMA and WCDMA power amplifiers

T. Fowler, K. Burger, Nai-Shuo Cheng, A. Samelis, E. Enobakhare and S. Rohlfing. "Efficiency improvement techniques at low power levels for linear CDMA and WCDMA power amplifiers." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 41-44.

Power amplifiers are significant contributors to current consumption within mobile phones resulting in continued focus on design techniques to improve power amplifier efficiency. The techniques presented here use variable bias current and supply voltage, which allow enhanced efficiency at low power levels, and variable load impedance, which provides tradeoffs for optimum linearity and efficiency as a function of power level and battery voltage.

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